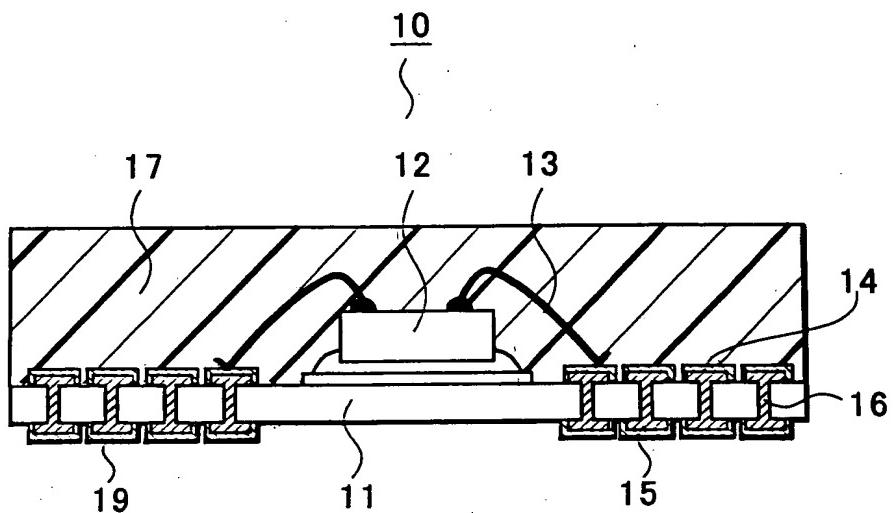
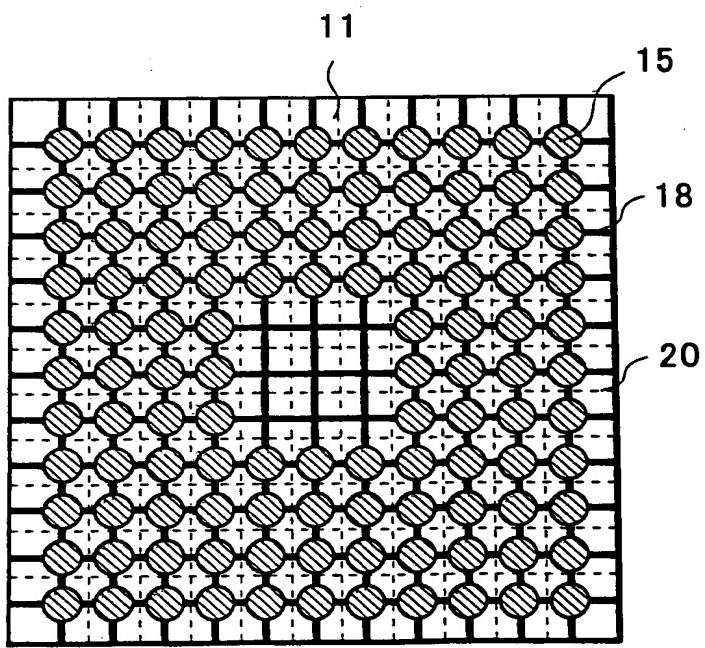
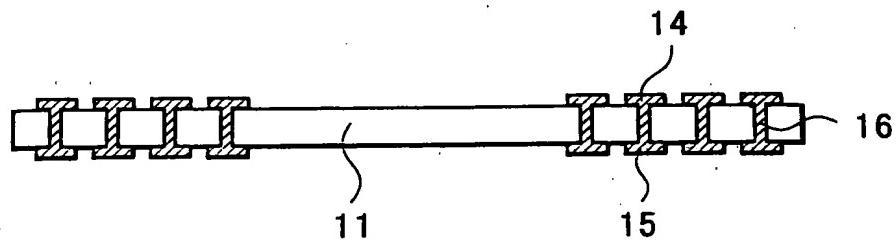
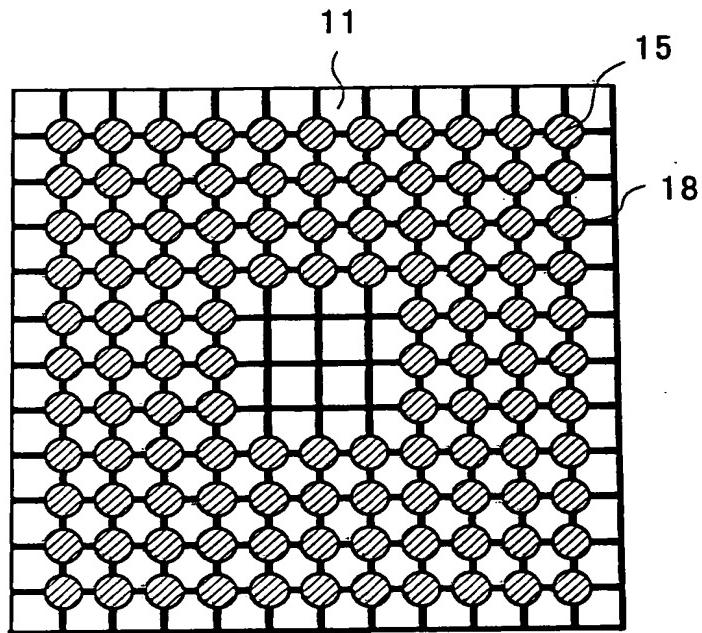
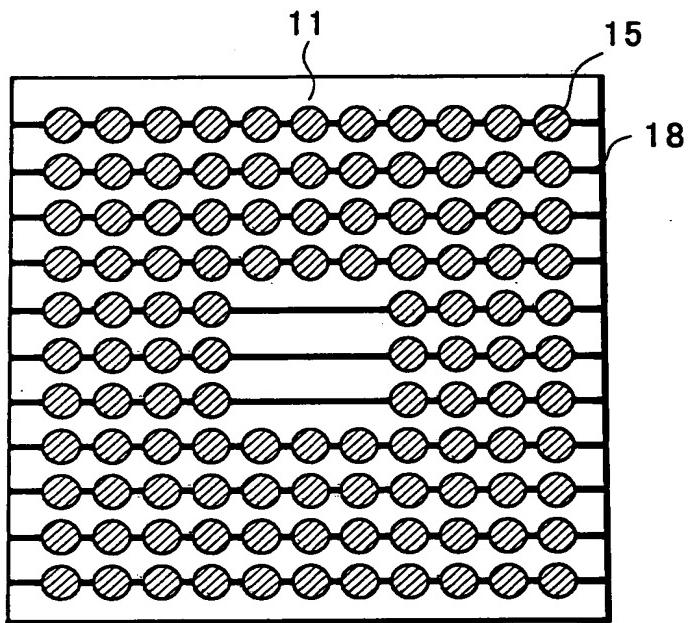


Applicant: Kiyoshi Mita

METHOD FOR MANUFACTURING MOUNTING SUBSTRATE
AND METHOD FOR MANUFACTURING CIRCUIT DEVICE**BEST AVAILABLE COPY****FIG.1A****FIG.1B**

BEST AVAILABLE COPY**FIG.2A****FIG.2B**

BEST AVAILABLE COPY**FIG.3**

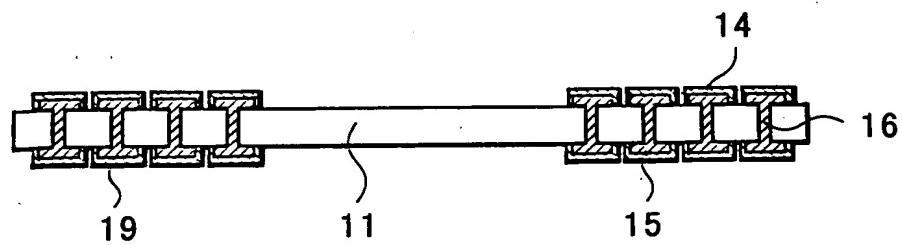
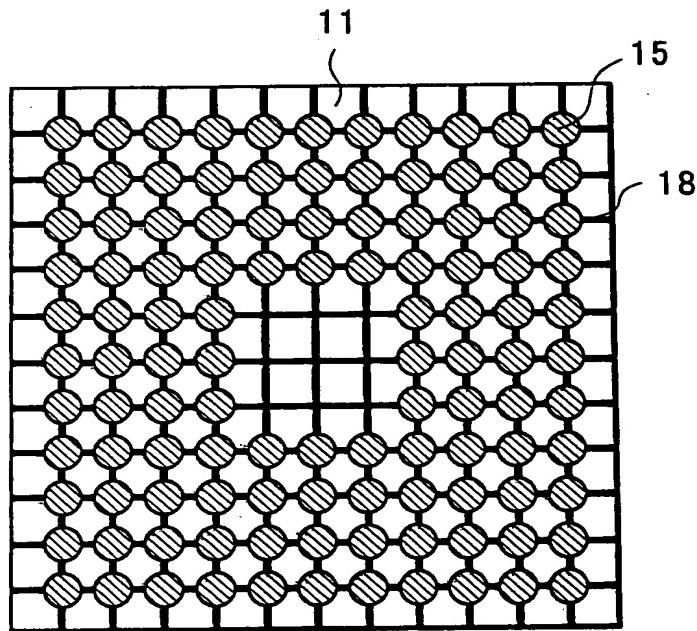
BEST AVAILABLE COPY**FIG.4A****FIG.4B**

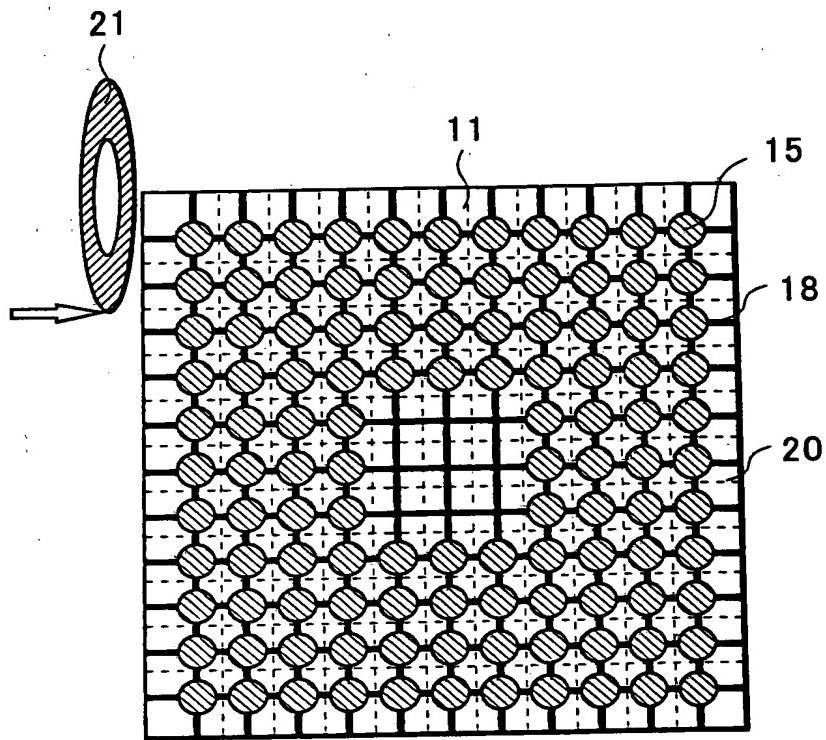
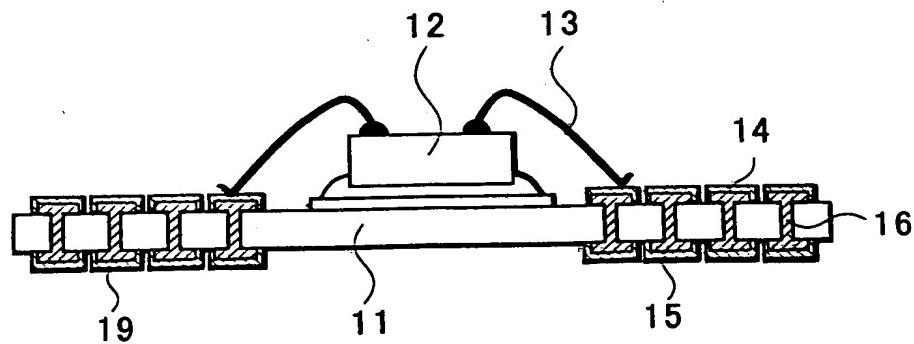
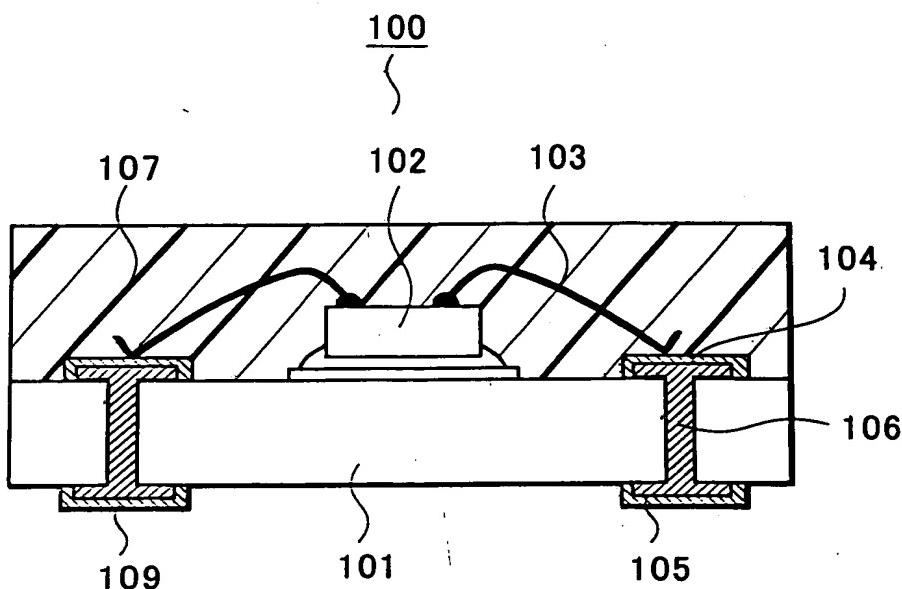
FIG.5**BEST AVAILABLE COPY****FIG.6**

FIG.7A**BEST AVAILABLE COPY****FIG.7B**